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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/408,430	09/29/1999	KENDELL A CHILTON	07072/087001	4043

22494 7590 09/24/2002
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EXAMINER

CHUNG TRANS, XUONG MY

ART UNIT PAPER NUMBER

2181

DATE MAILED: 09/24/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

1/R

Office Action Summary

Application No.

09/408,430

Applicant(s)

CHILTON, KENDELL A

Examiner

Xuong M. Chung-Trans

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 28 March 2002.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2&3.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

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1. This application has been examined.
2. Claims 1-7 are pending in this application.
3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) do not apply to the examination of this application as the application being examined was not (1) filed on or after November 29, 2000, or (2) voluntarily published under 35 U.S.C. 122(b). Therefore, this application is examined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

4. Claims 1-7 are rejected under 35 U.S.C. 102(e) as being anticipated by Temple (U.S. Pat. No. 5,875,342).

As per claim 1, Fig. 2 of Temple discloses an interrupt request controller 200 including an interrupt masking means 202 adapted to mask selected ones of the interrupt request signals

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generated by the computer system 100 (col. 5, lines 25-28) and a priority control unit 204 connected to the interrupt masking means 202 for coupling unmasked ones of the interrupt signals to a plurality of outputs in accordance with a predetermined priority criteria (col. 5, lines 29-47).

As per claims 2 and 3, Temple discloses an interrupt register 201 connected to receive the interrupt request signals for storing a bit in the register for each one of the interrupt signals representing whether the logic state of the interrupt signal should be a logic "1" value or a logic "0" value (col. 5, lines 16-20 and 32-39). Further, Temple discloses that the interrupt signals are selectively asserted by the interrupt request controller 200 (col. 5, lines 25-28).

As per claim 4, as disclosed in Temple the logic state "1" of the interrupt logic signal is an edge and the logic state "0" of the interrupt logic signal is a level.

As per claims 5-7, these claims recite substantially all of the limitations as defined in claims 1-4; therefore, they are rejected under the similar rationale.

5. The additional cited references are considered as art being relevant to this application. Applicant is requested to consider them when responding to this Office action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Xuong M. Chung-Trans whose telephone number is (703) 305-9772. The examiner can normally be reached on Monday-Friday from 9:30 AM to 2:30 PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Peter Wong, can be reached on (703) 305-3477. The fax phone number for the organization where this application or proceeding is assigned is (703) 746-7239.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.



X.M. Chung-Trans
Patent Examiner
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